

Amendments to the Claims:

The following listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Previously Presented) An electro-optical device comprising:

a substrate;

data lines formed above the substrate and extending in a predetermined direction and scanning lines formed above the substrate and extending in a direction intersecting the data lines;

switching elements to which scanning signals are supplied from the scanning lines;

pixel electrodes to which image signals are supplied from the data lines via the switching elements;

a relay electrode that electrically connects one of the switching elements to one of the pixel electrodes;

an image display region defined as a region of the substrate in which the pixel electrodes and the switching elements are formed;

a peripheral region defining the periphery of the image display region;

a driver disposed in the peripheral region;

exterior circuit connection terminals comprising electrodes provided in the peripheral region at a position between the driver and a peripheral edge of the substrate;

storage capacitors comprising capacitor electrodes to retain potentials of the pixel electrodes for a predetermined period of time; and

a capacitor wire which supplies voltage to the capacitor electrodes,

wherein the capacitor wire, the electrodes of the exterior circuit connection terminals and the relay electrode are each formed of a same material and in the same film.

2. (Original) The electro-optical device according to Claim 1, the capacitor wire formed on the data lines with a first interlayer insulating film interposed therebetween.

3. (Original) The electro-optical device according to Claim 1, the capacitor wire formed in a layer located immediately under a layer including the pixel electrodes.

4. (Original) The electro-optical device according to Claim 1, the capacitor electrodes provided below the data lines with a second interlayer insulating film interposed therebetween.

5. (Original) The electro-optical device according to Claim 1, further comprising:
a scanning line drive circuit, a potential supplied to the capacitor wire including a potential supplied to the scanning line drive circuit.

6. (Original) The electro-optical device according to Claim 1, further comprising:
a counter substrate and a counter electrode provided above the counter substrate;

a potential supplied to the capacitor wire including a potential supplied to the counter electrode.

7. (Original) The electro-optical device according to Claim 1, the capacitor wire including a shading material.

8. (Original) The electro-optical device according to Claim 1, the capacitor wire having a multilayer structure including different materials.

9. (Original) The electro-optical device according to Claim 1, the capacitor wire having a lattice pattern in the image display region when viewed in plan.

10. (Original) The electro-optical device according to Claim 9, the capacitor wire formed in the lattice pattern having intersections each having at least one of approximately triangle shaped section at least one of four corners of the intersections.

11. (Original) The electro-optical device according to Claim 1, further comprising:
a step-adjusting film under a region corresponding to the exterior circuit connection terminals, the step-adjusting film adjusting the height of the capacitor wire and that of the exterior circuit connection terminals to be approximately equivalent to each other with respect to the surface of the substrate.

12. (Original) An electronic apparatus, comprising:
the electro-optical device according to Claim 1.

13. (Previously Presented) The electro-optical device according to Claim 1, the capacitor wire, the electrodes of the exterior circuit connection terminals and the relay electrode each having a two-layered structure formed of an aluminum-based layer as a lower layer and a titanium nitride-based layer as an upper layer.

14. (New) The electro-optical device according to claim 1, the capacitor wire, the electrodes of the exterior circuit connection terminals and the relay electrodes each having a plural layered structure including an aluminum-based layer and a layer based on a metal other than aluminum.